AMENDMENTS TO THE CLAIMS

This listing of claims replaces all previous versions and listings of claims in this application:

Claims 1-4 (Canceled).

5. (Previously presented) An amplifier circuit suitable for amplifying an AM broadcast signal, the circuit comprising:

a first P-channel MOSFET which amplifies the inputted AM broadcast signal; and

a second P-channel MOSFET which controls a gain of a signal outputted from said first P-channel MOSFET;

wherein said first P-channel MOSFET and said second P-channel MOSFET are cascode-coupled in a manner which ensures that a flicker noise level of the amplifier circuit is intermediate to respective flicker noise levels of a JFET-configured circuit and an N-MOS configured circuit.

6. (Previously presented) An amplifier circuit suitable for amplifying an AM broadcast signal, the circuit comprising:

a first P-channel MOSFET which amplifies the AM broadcast signal;

a second P-channel MOSFET connected to the first P-channel MOSFET, said second P-channel MOSFET controlling a gain of a signal outputted from said first P-channel MOSFET; and

a tuning circuit connected to a drain of the second P-channel MOSFET, said tuning circuit filtering and providing an output from said second P-channel MOSFET;

wherein said first P-channel MOSFET and said second P-channel MOSFET are configured in a cascode-coupled arrangement which reduces a flicker noise of the amplifier circuit below a flicker noise level of an equivalent N-MOS configured circuit.

7. (Previously presented) An amplifier circuit suitable for amplifying an AM broadcast signal, the circuit comprising:

a capacitor which blocks a DC component of the AM broadcast signal;

a first P-channel MOSFET connected to an output of the capacitor, said first P-channel MOSFET amplifying the AM broadcast signal;

a resistor which biases the first P-channel MOSFET;

a second P-channel MOSFET connected to the first P-channel MOSFET so as to control a gain of a signal outputted from said first P-channel MOSFET; and

a tuning circuit which filters and outputs the signal from said second P-channel MOSFET;

wherein said first P-channel MOSFET and said second P-channel MOSFET are configured in a cascode-coupled arrangement which reduces a flicker noise of the amplifier circuit below a flicker noise level of an equivalent N-MOS configured circuit.

8. (Previously presented) The amplifier circuit of claim 5, wherein a channel area of said P-channel MOSFET is selected to reduce a flicker noise of the amplifier circuit.

9. (Previously presented) The amplifier circuit of claim 6, wherein a channel area of said P-channel MOSFET is selected to reduce a flicker noise of the amplifier circuit.

10. (Previously presented) The amplifier circuit of claim 7, wherein a channel area of said P-channel MOSFET is selected to reduce a flicker noise of the amplifier circuit.

Claims 11-14 (Canceled).